

Docket No.: 57454-309



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

✓ Shigeki TOMISHIMA

✓ Serial No.: 09/996,574

Group Art Unit: 2826

✓ Filed: November 30, 2001

Examiner: Victor A. MANDALA, JP

✓ For: SEMICONDUCTOR MEMORY DEVICE

RECEIVED
JAN 28 2003
TECHNICAL DIVISION 2800

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
Washington, DC 20231

Sir:

In response to the non-final Official Action dated September 24, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claim 1 as follows:

1. (Amended) A semiconductor memory device comprising:

a plurality of memory cells arranged in rows and columns;

a plurality of sub word lines, provided corresponding to the respective memory cell rows, each having memory cells on a corresponding row connected thereto;

a plurality of main word lines, each provided corresponding to a prescribed number of sub word lines in said plurality of sub word lines and disposed in a first conductive layer, for transmitting a row select signal;